

FORM PTO-1449		Docket Number (Optional) 100-19410 (P05247-D01)	Application No. New (Div. of 10/272,035)
INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i> Express Mail Number <u>EV364033438US</u>		Applicant(s) James Thomas Doyle et al.	
		Filing Date Herewith	Group Art Unit Unknown
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
UKT	*Dragan Maksimovic, Bruno Kranzen, Sandeep Dhar and Ravindra Ambatipudi, U.S. Patent Application No. 10/053,226, filed January 19, 2002, entitled "An Adaptive Voltage Scaling Digital Processing Component and Method of Operating the Same".		
UKT	*Bruno Kranzen and Dragan Maksimovic, U.S. Patent Application No. 10/053,227, filed January 19, 2002, entitled "Adaptive Voltage Scaling Clock Generator for Use in a Digital Processing Component and Method of Operating the Same".		
UKT	*Dragan Maksimovic and Sandeep Dhar, U.S. Patent Application No. 10/053,828 , filed January 19, 2002, entitled "System for Adjusting a Power Supply Level of a Digital Processing Component and Method of Operating the Same". <u>10/053,858</u> UKT		
UKT	*Dragan Maksimovic, Ravindra Ambatipudi, Sandeep Dhar and Bruno Krazen, U.S. Patent Application No. 10/053,228, filed January 19, 2002, entitled "An Adaptive Voltage Scaling Power Supply for Use in a Digital Processing Component and Method of Operating the Same". <u>106,428</u> UKT		
UKT	*James T. Doyle and Dragan Maksimovic, U.S. Patent Application No. 10/106,428, filed March 26, 2002, entitled "Method and System for Minimizing Power Consumption in Mobile Devices Using Cooperative Adaptive Voltage and Threshold Scaling".		
UKT	*Dragan Maksimovic and James Thomas Doyle, U.S. Patent Application No. 10/166,822, filed June 10, 2002, entitled "Serial Digital Communication Superimposed on a Digital Signal Over a Single Wire".		
UKT	*Dragan Maksimovic and Sandeep Dhar, U.S. Patent Application No. 10/236,482, filed September 6, 2002, entitled "Method and System for Providing Self-Calibration for Adaptively Adjusting a Power Supply Voltage in a Digital Processing System".		
UKT	*Mark F. Rives, U.S. Patent Application No. 10/246,971, filed September 19, 2002, entitled "Power Supply System and Method that Utilizes an Open Loop Power Supply Control".		
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UKT	*Robert W. Erickson and Dragan Maksimovic, <u>Fundamentals of Power Electronics</u> , Second Edition, Kluwer Academic Publishers, 2001, pp. 333.		
UKT	*Krisztian Flautner, Steven Reinhardt and Trevor Mudge, Automatic Performance Setting for Dynamic Voltage Scaling, Wireless Networks, Volume 8, Issue 5, September 2002, pps. 507-520, and Citation, pps. 1-3, [online]. [retrieved on 2003-02-02]. Retrieved from the Internet: <URL: http://portal.acm.org/citation.cfm?id=582455.582463&coll=portal&dl=ACM&idx=J804&p... >.		
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UKT	*Texas Instruments, "Synchronous-Buck PWM Controller With NMOS LDO Controller", TPS5110, SLVS025A-April 2002, Revised June 2002.		
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UKT	*Oliver Weinfurter, "Switcher Output Stages on Neptune 28 (CMOS7-5), Neptune 28 Output Stage Results, September 24, 2001, pps. 1-8.		
UKT	*Intel XScale Core, Developer's Manual, December 2000, [online], [retrieved on 2003-02-02]. Retrieved from the Internet: <URL: http://developer.intel.com/design/intelxscale/27347301.pdf >. pps. 1-1 through B-1.		
EXAMINER	LONG TRAN	DATE CONSIDERED	1/12/05
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.			